



10/658,710
C65

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
(Case No. 01-1205-A)

In re U.S. Patent of:)
Alon et al.) Filing Date: September 8, 2003
Patent No.: 6,919,749 B2) Application No.: 10/658,710
Issue Date: July 19, 2005)
For: Apparatus and Method for a Digital)
Delay Locked Loop)

TRANSMITTAL LETTER

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Certificate
AUG 05 2005
of Correction

In regard to the above-identified patent application:

1. We are transmitting herewith the attached:
 1. Request to Issue Certificate of Correction
 2. Certificate of Correction
 3. Return postcard
2. With respect to additional fees:
 - A. No additional fee is required.
 - B. Attached is a check in the amount of \$_____.
 - C. Charge the total additional fee of \$100.00 to Deposit Account No. 13-2490.
3. Please charge any additional fees or credit overpayments to the Deposit Account No. 13-2490. A duplicate copy of this sheet is enclosed.
4. CERTIFICATE OF MAILING UNDER 37 CFR 1.8 (FIRST CLASS MAIL): The undersigned hereby certifies that this Transmittal Letter and the document(s) as described in paragraph 1 hereinabove, are being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 28 day of July 2005.

By:

Paul W. Churilla
Reg. No. 47,495



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REQUEST TO ISSUE CERTIFICATE OF CORRECTION

Dear Sir:

Pursuant to C.F.R. § 1.322, it is hereby requested that a Certificate of Correction be issued to correct minor errors of form in the above-referenced patent. The request corrections are indicated in the Certificate of Correction attached herewith. Please charge the \$100 fee under 37 C.F.R. § 1.20(a) to deposit account 13-2490. Also, charge any additional fees or credit any overpayments to deposit account 13-2490.

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Respectfully Submitted,

McDonnell Boehnen Hulbert & Berghoff LLP

Date: July 28, 2005 By: Paul W. Churilla

Paul W. Churilla
Reg. No. 47,495

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO.: 6,919,749 B2

DATED: July 19, 2005

INVENTOR(S): Alon et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In claim 3, column 13, line 65, please change that line to read:

" the second storage device; and"

In claim 10, column 14, line 64, please change that line to read:

" a second input terminal for receiving a second signal;"

In claim 10, column 15, line 24, please change that line to read:

" the second storage device; and;"

In claim 10, column 15, lines 30-31, please change those lines to read:

" multiplexor, the fourth multiplexor, the fifth multiplexor, and the sixth multiplexor."

In claim 17, column 16, line 7, please change that line to read:

" 17. A circuit comprising:
a first delay line having a first set of output taps and"

In claim 17, column 16, line 39, please change that line to read:

" the second storage device; and"

MAILING ADDRESS OF SENDER:

PATENT NO. 6,919,749 B2

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